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AMD reveals more details on its non-x86 server chip strategy with a 2013-2014 server roadmap detailing launches following the availability of the "Kyoto" Opteron X-Series small-core server processor.

The first ARM-based server SoC from AMD is "Seattle"-- a 64-bit chip featuring Cortex-A57 architecture. It supports up to 128GB of RAM, integrated 10Gb ethernet and AMD "Freedom Fabric" technology grouping low-power CPU cores into clusters.

The 8- and 16-core SoCs will run at 2GHz and offer 2-4 times the performance of the x86-based low-power Opteron CPUs (a figure apparently based on boosted core counts, really).

The company also announces a duo of x86 server processors-- "Berlin" (a quad-core Steamroller-based chip available as either APU or CPU) and "Warsaw" (a 12- and 16-core Piledriver-based CPU for servers with 2 or 4 CPU sockets).

Seattle sampling starts on Q1 2014 before production kicks off during H2 2014, while Berlin and Warsaw will be available on H1 2014.

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